

Computer Engineering

THE UNIVERSITY OF TEXAS AT DALLAS



Accept no limits.

UTD's research centers integrate electrical engineering and computer science resources, giving UTD the "devices to systems" capability to be successful.

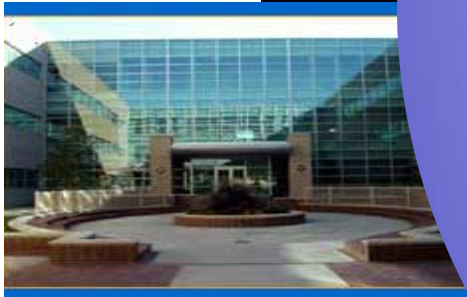
System Level Design

Software Systems

Networking &
Communications

Computer
Applications

Circuits &
Devices



Proven track record on large-scale government, industry and university partnerships brings cutting edge research and education for evolving technologies.

Research Centers

CATSS

Center for Advanced Telecommunications Systems and Services

forward-looking research and development in wireless and optical networking

CICS

Center for Integrated Circuits and Systems

excellence in integrated circuits and systems design

CSCSP

Center for Systems, Communications, and Signal Processing

Partnerships in systems, communications, and signal processing

EAC

Embedded and Adaptive Computing Group

Aggressive research and education in embedded and adaptive computing

ESC

Embedded Software Center

Dramatically increasing the productivity and quality of complex embedded applications

PhoTEC

Photonic Technology & Engineering Center

The science and engineering of information transmission via light

Computer Engineering Program
Erik Jonsson School of Engineering & Computer Science
Program Office: ECS North 4.702
972-883-4663

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Research in Computer Engineering...

Engineer solutions with the faculty at UTD.

Computer Engineering Faculty and Their Research Interests

Poras T. Balsara, Ph.D., Penn State University

Techniques for the design of energy-efficient digital systems, VLSI circuits and architectures for telecommunications and DSP applications, reconfigurable computing and computer arithmetic

Dinesh K. Bhatia, Ph.D., University of Texas at Dallas

Architecture and CAD for FPGAs, Reconfigurable and Adaptive Computing, Physical Design Automation, System Level Design, Embedded Systems, Engineering Education.

Cyrus Cantrell, Ph.D., Princeton University

Photonics, including Raman amplification in fibers, nonlinear fiber optics, optical switching and routing; optical networks

Jorge Cobb, Ph.D., University of Texas Austin

Quality of service scheduling in computer networks, stabilizing systems, mobile computing

Kamran Kiasleh, Ph.D., University of Southern California

Communications systems, wireless communications, CDMA, channel estimation techniques, architecture and performance evaluation of ad-hoc wireless networks, coding and modulation for unguided (Infrared) optical channels, and multiple access techniques for wireless access

Mehrdad Nourani, Ph.D., Case Western Reserve University

Design for testability, system-on-Chip testing signal integrity modeling and test, packet processing architecture.

Ivor Page, Ph.D., Brunel University, U.K.

Distributed algorithms, including resource allocation problems, and in computer graphics

Issa Panahi, Ph.D., University of Colorado at Boulder

Digital Signal processing-filtering, spectrum estimation, system identification, active noise control, processor-based system design and applications

Ravi Prakash, Ph.D., Ohio State University

Operating systems, distributed algorithms, mobile computing

Rama Sangireddy, Ph.D., Iowa State University

Computer Architecture, Adaptive Computing Systems, Fault-Tolerant systems design, Computer Communications & Networks.

Edwin Sha, Ph.D., Princeton University

Embedded Systems, High-performance Computer and Network Architectures, Computer Security, Compilers, Hardware/Software Co-Designs, High-Level Synthesis, Parallel Architectures, Real-Time Systems.

S. Venkatesan, Ph.D., University of Pittsburgh

Fault tolerance, high availability, security, performance of computer systems, computer networks, distributed computing.

Yuke Wang, Ph.D., University of Saskatchewan, Canada

Low power ASIC, DSP Processor architecture and implementation, wireless communication, network security, network QoS.

Kang Zhang, Ph.D., University of Brighton, U.K.

Distributed programming, visual languages, Web engineering

Youtao Zhang, Ph.D., University of Arizona

Program analysis, profiling and code optimization, Computer architecture, Embedded systems and Secure processors

Si Qing Zheng, Ph.D., University of California, Santa Barbara

Algorithms, Architectures, HW/SW Co-design, Real-time/Embedded Systems, Parallel and Distributed Processing, Telecommunications and Networks, VLSI

Dian Zhou, Ph.D., University of Illinois

VLSI design, CAD tools and algorithms, and mixed signal circuits